Amendments to the Specification:

Please replace the paragraph beginning on page 5, line 7 with the following rewritten paragraph.

Fig. Figs. 5A-D are is a more detailed schematic schematics of one implementation of a line side circuit as shown in Figs. 1 and 2;

Please replace the paragraph beginning on page 5, line 13 with the following rewritten paragraph.

There is shown in Fig. 1 a modem 10 including a data access arrangement (DAA) 12 a system interface 14, and a jack 16. Jack 16 is typically an RJ 11 jack and receives a compatible plug from a typical telephone line 18. The RJ 11 or similar jack may be on the end of a cable remote from DAA 12 as shown or it may be combined with and on the same printed circuit board 17 for example as DAA 12 as shown in phantom at 18 18. Printed circuit board 17 forms the platform for DAA 12 which includes the system side circuit 20, line side circuit 22 and an isolation circuit 24 here indicated as a capacitive coupling. The host may be any desired system typically the computer or microprocessor 26.

Please replace the paragraph beginning on page 5, line 22 with the following rewritten paragraph.

In accordance with this invention a portion of the data access arrangement is included in jack housing 16a which includes contacts 30, Fig. 2, which engage the mating contacts from a compatible plug such as an RJ 11 or other public service telephone network plug. Line side circuit 22 is included within jack housing 16a along with a portion of the isolation circuit 24. As indicated in Fig. 2, the isolation circuit may be capacitive as shown at 24 or inductive as indicated by the presence of transformer 24a shown in phantom or could be optical isolation as well. Jack housing 16a is shown in greater mechanical detail in Fig. 3. Contacts 30 which engage with contacts 32 on RJ 11 plug 34 are disposed on the lower surface 36 of jack housing 16a. In a rearward portion of surface 36 is located printed circuit board 17 which includes line side circuit 22 and may include additionally the isolation circuit and other auxiliary circuits. These may alternatively be on the motherboard or add-in card 40. Jack housing 16a includes terminals 42 which may be threaded screws, solder pins, or any other suitable device which allow it to be easily mounted and often easily dismounted as well to motherboard or add-in card 40. In this way, jack housing 16a with its included modem circuitry is decoupled from the motherboard so that its certification or homologation may be accomplished independent of any effect on the motherboard or its host system. Circuit board 17 is shown in greater detail at Fig. 4 as including not just line side circuit 22 but also the diode bridge 50 to guard against reverse connection of the telephone instrument, a protection circuit 54 which may include a lightning suppression circuit and a fuse as

well as other devices. The fuse is typically a UL 1950 fuse. In this particular embodiment the isolation circuit is implemented with capacitors 24' and in this case they are located right on printed circuit board 17 within jack housing 16a. Although this is not a necessary limitation of the invention they may as well be on the motherboard or on some other printed circuit board or chip. The capacitors 24' that implement the isolation circuit are connected directly to pins 42, Fig. 4. Printed circuit board 17 also includes compliant circuits 43 which are used to tailor the circuit to the particular standards of the country or region in which it is to be used. Fig. Figs. 5A-D show shows one specific implementation of the line side circuit 22 and its associated circuitry including but not limited to the isolation circuit 24, compliant circuit 43, diode bridge 50, and protection circuit 54. Protection circuit 54 includes both UL 1950 fuse 60 and lightning suppression circuitry 62. The device side circuit 20 is shown in greater detail in Fig. 6.

Please replace the page 11 of the specification (the page containing the Abstract of the Disclosure) with the following substitute page.